



Note 1 R15 sets the switching delay for the MOSFETs. 22K is the minimum allowed.

Note 2 R8 and C5 set the overload delay. Present values give about 30 milliseconds.

Note 3 The DC input can be increased to 30v if R14 and R18 are increased to 1K5
The limit is now set by the 7805 regulator input.

Note 4 C12 and R19 are an output waveform filter.
The values may need adjusting to give the best waveform.

Note 5 Opto isolators U1 and U3, type TLP521-1A, KB814 or equivalent.

Note 6 The value of R1 shown is for a DCC input voltage of 12 to 18V
If using a logic level drive, reduce R1 to 470 ohms.

DCC booster type NB1A version 2

Mike Bolton 06/12/08



Issue 2 Rev A